

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A digital data filtering circuit, ~~comprising~~ able to implement the steps of:

~~a discrete transform means to calculate~~ calculating a discrete transform (DCT2N) data item set of a set of 8 original data (w) items, the set of transformed data items comprising a last even and a last 2 even data items, and a last odd, a last 2 odd and a last 3 odd data items having the highest frequencies in the set of transformed data (W),

~~an inverse discrete transform means to calculate~~ calculating an inverse discrete transform (IDCT2N) of the set of transformed data (W) thus obtained,

~~said circuit being able to filter at least one data item among the set of transformed data (W), and being characterized in that it comprises:~~

a first odd filtering module (FILo1) (~~FILO1~~) ~~intended to filter the last odd transformed data item or the last 3 odd transformed data items having the highest frequencies in the set of transformed data (W), and~~

a second odd filtering module (FILo2) connected to the first filtering module ~~and intended to filter the last 2 odd transformed data items having the highest frequency in the set of transformed data (W).~~

2. (currently amended) A filtering circuit as claimed in claim 1, wherein the ~~comprising~~ discrete transform means (DCTN) ~~intended is further configured to~~ successively transform a first half (u) of the set of original data and a second half (v) of the set of original data into a first transformed data half (U) and a second transformed data half (V), said circuit also comprising an even ~~a third~~ filtering module (FILE) ~~intended to filter the last even transformed data item or the last 2 even transformed data~~

items having the highest frequency in the set of transformed data (W) using part of the first and second transformed data halves (U,V).

3. (original) A filtering circuit as claimed in claim 1, where half of the data in the set of original data are data of even or odd rank in a segment of a first coding block and half are data with the same parity as a corresponding segment of a second coding block adjacent to the first coding block.
4. (original) A filtering circuit as claimed in claim 1, where half of the data in the set of original data are 4 data with the highest ranks in a segment of a first coding block and half are 4 data with the lowest ranks in a corresponding segment of a second coding block adjacent to the first coding block.
5. (original) A video decoder able to supply decoded digital images and comprising a filtering circuit as claimed in claim 1, able to filter the decoded digital images so as to supply filtered digital images.
6. (original) A portable apparatus comprising a video decoder as claimed in claim 5, able to display the processed digital images on a screen of said apparatus.
7. (original) A television receiver comprising a filtering circuit as claimed in claim 1, able to filter digital images received by said receiver so as to display filtered digital images on a screen of said receiver.